

Fig. 1

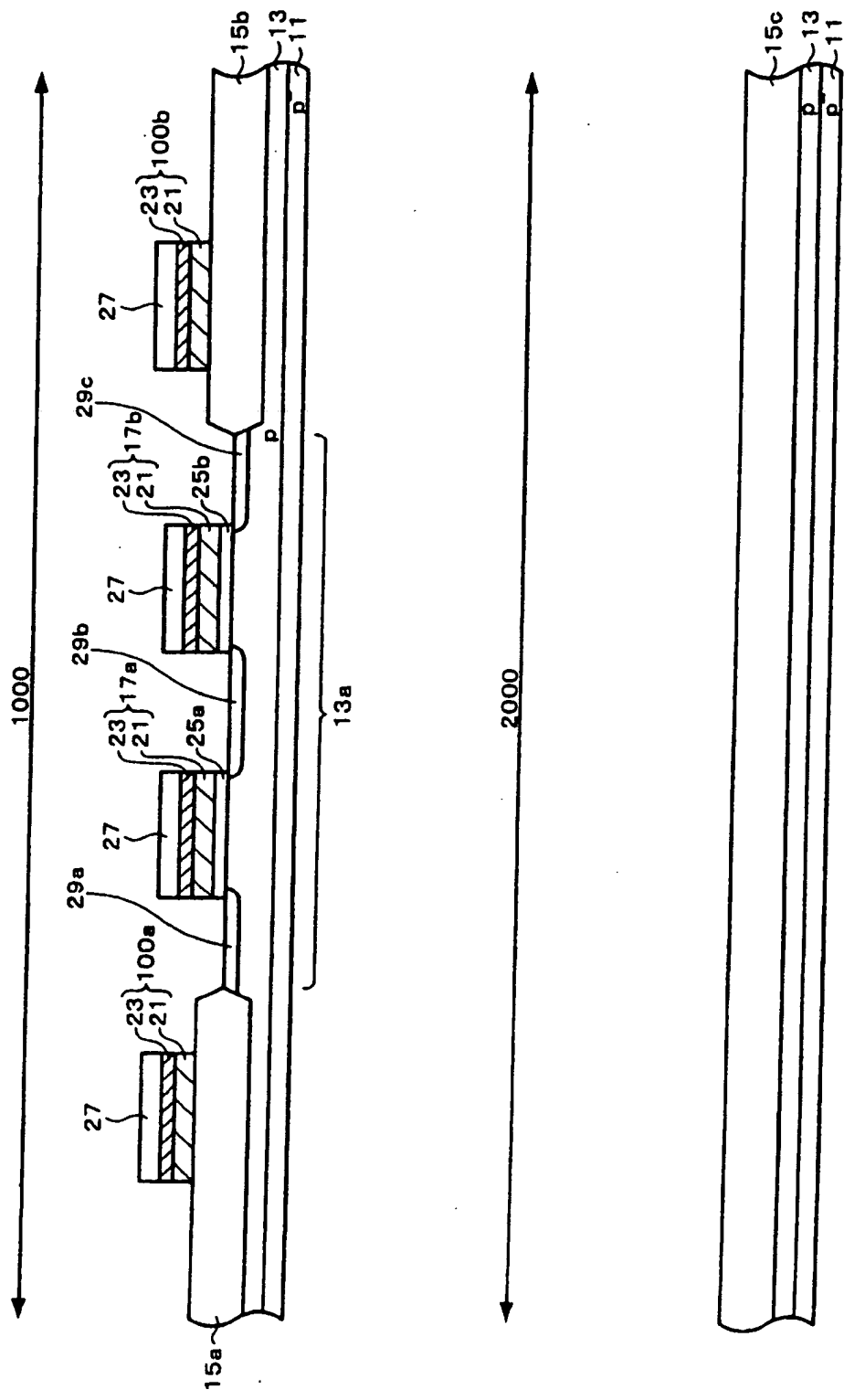


Fig. 2

FIG. 5 is a cross-sectional view of the device of FIG. 1, taken along line 5-5 of FIG. 1, showing the device in a second state of operation. The device is shown in a second state of operation, with the first layer 11 and the second layer 12 being in a second state of operation, and the third layer 13 being in a second state of operation. The device is shown in a second state of operation, with the first layer 11 and the second layer 12 being in a second state of operation, and the third layer 13 being in a second state of operation.

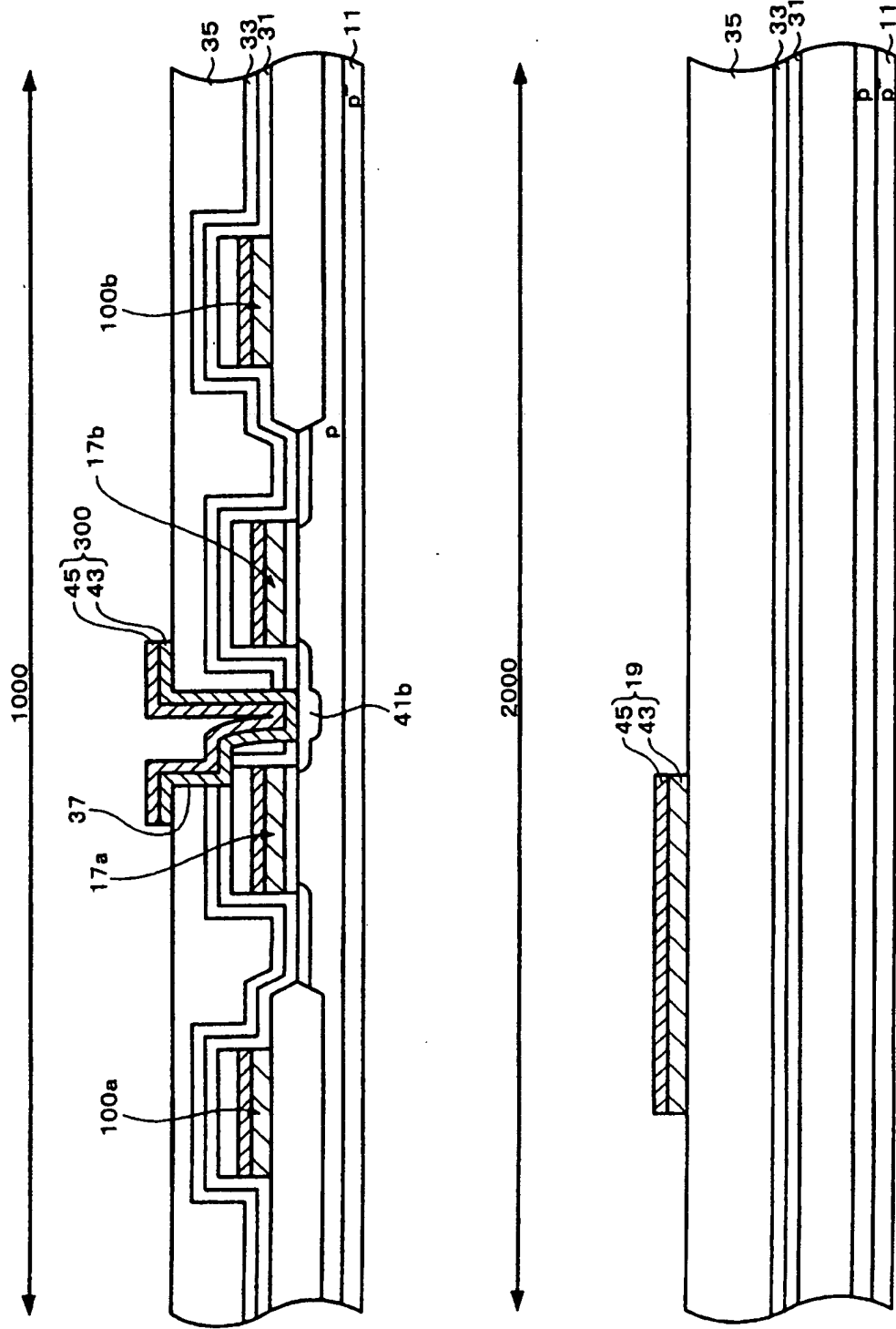


Fig. 5

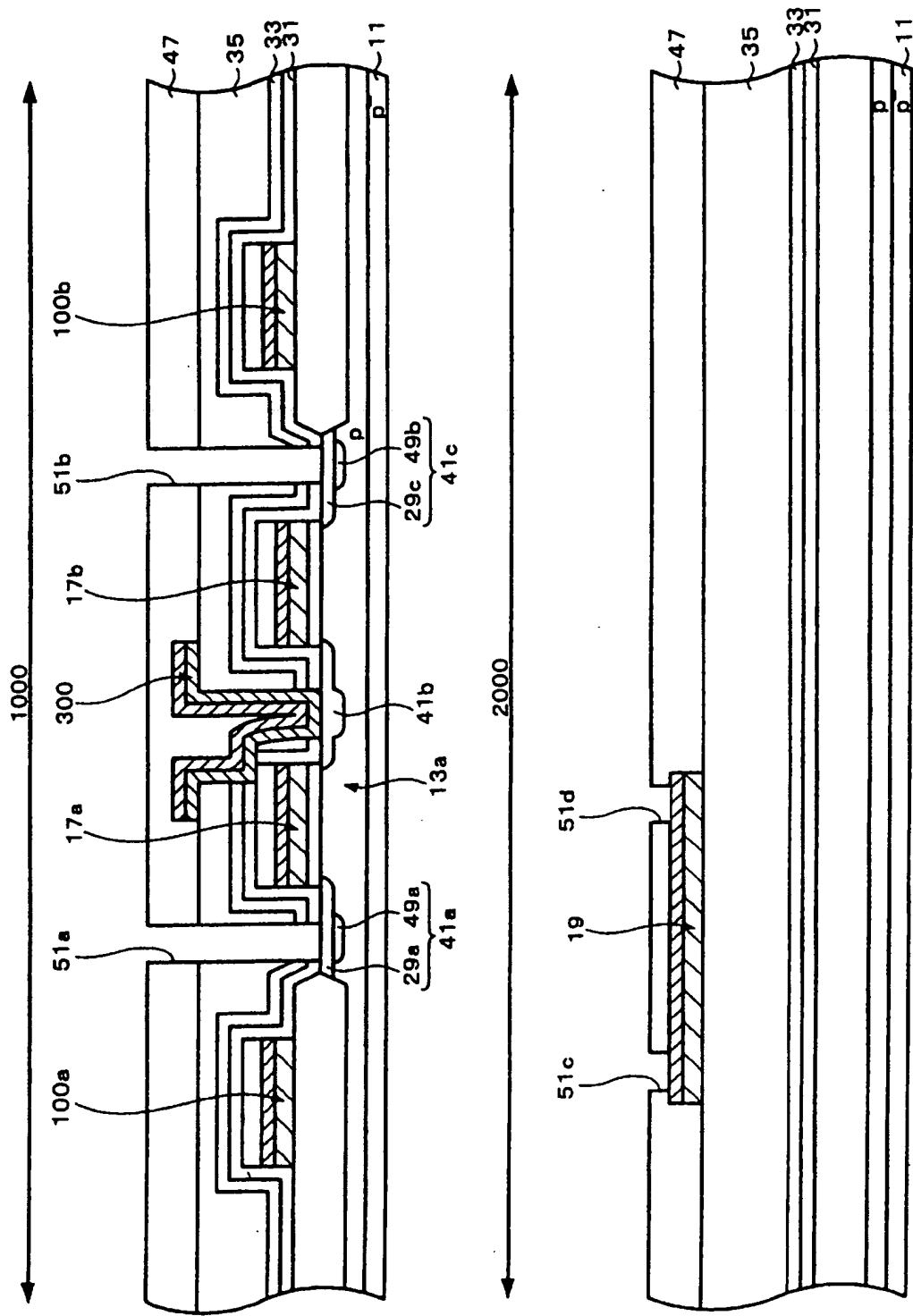


Fig. 6

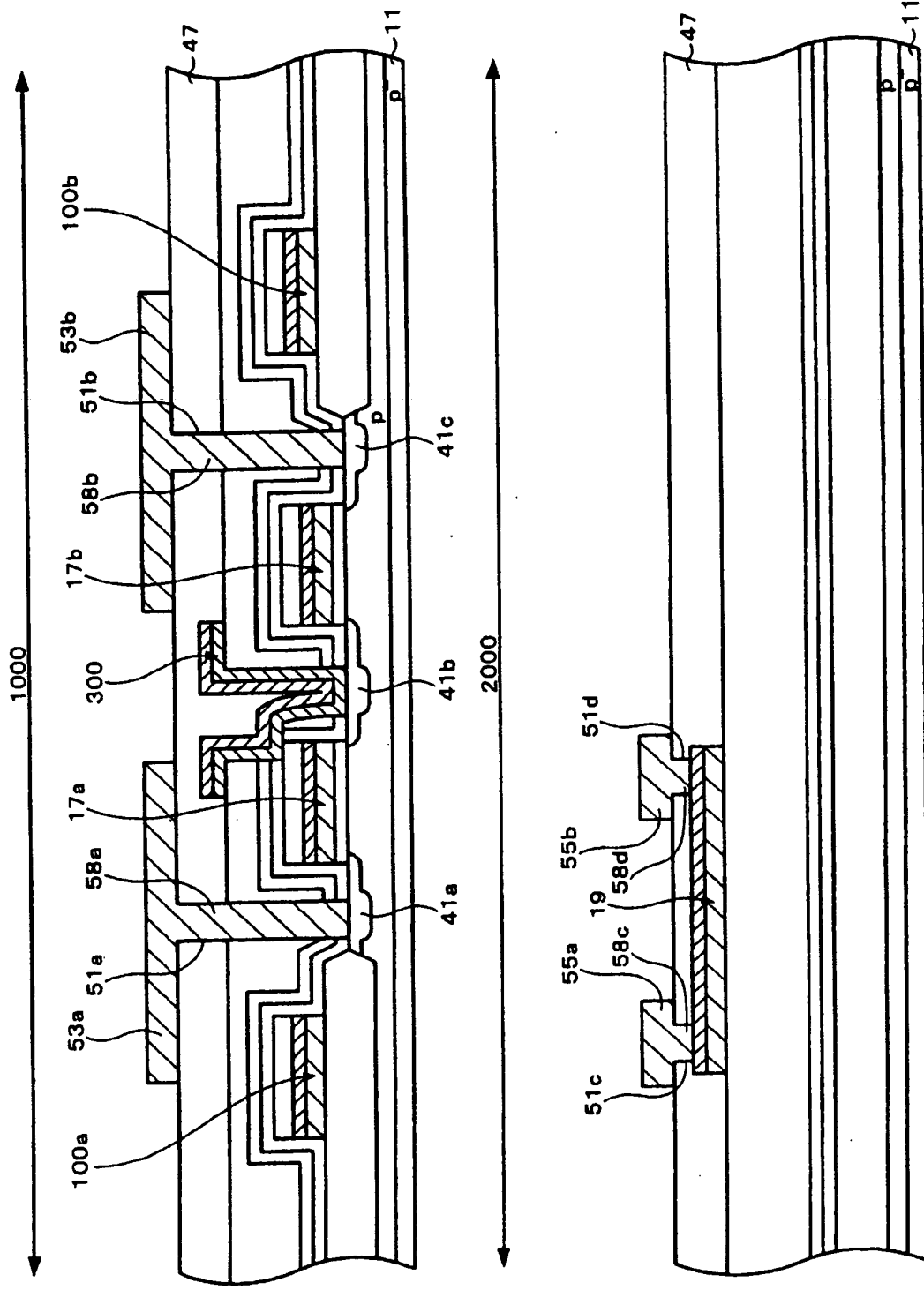


Fig. 7

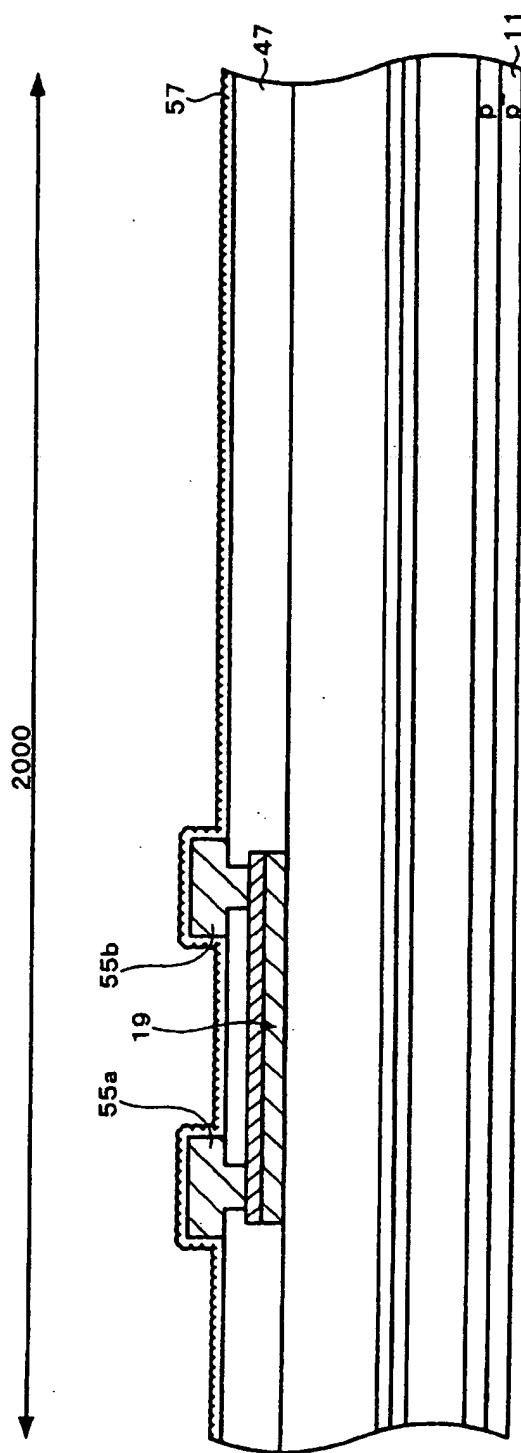
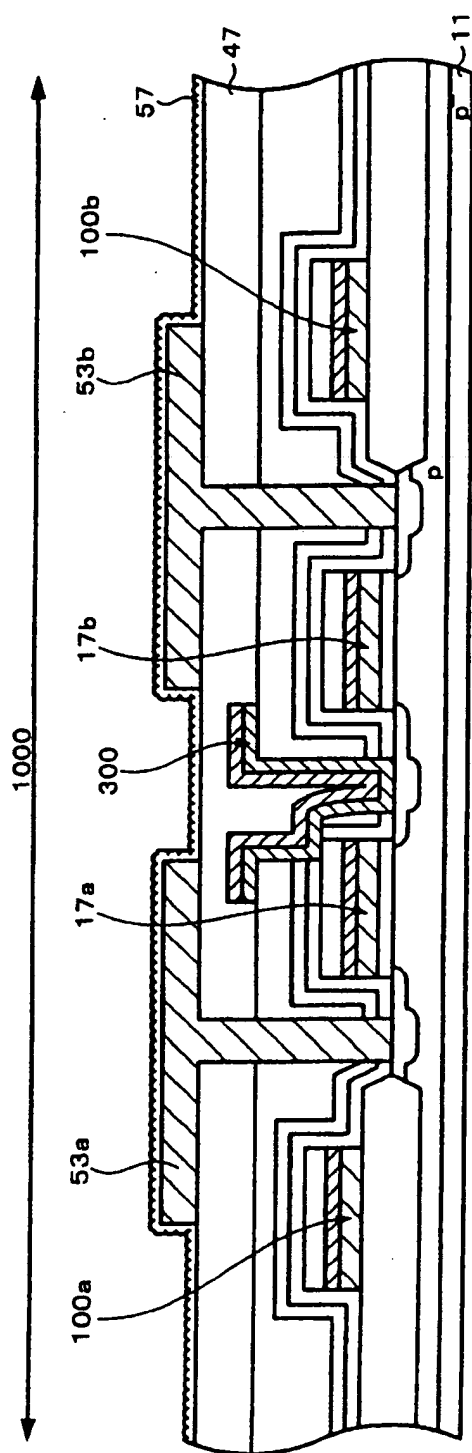


Fig. 8

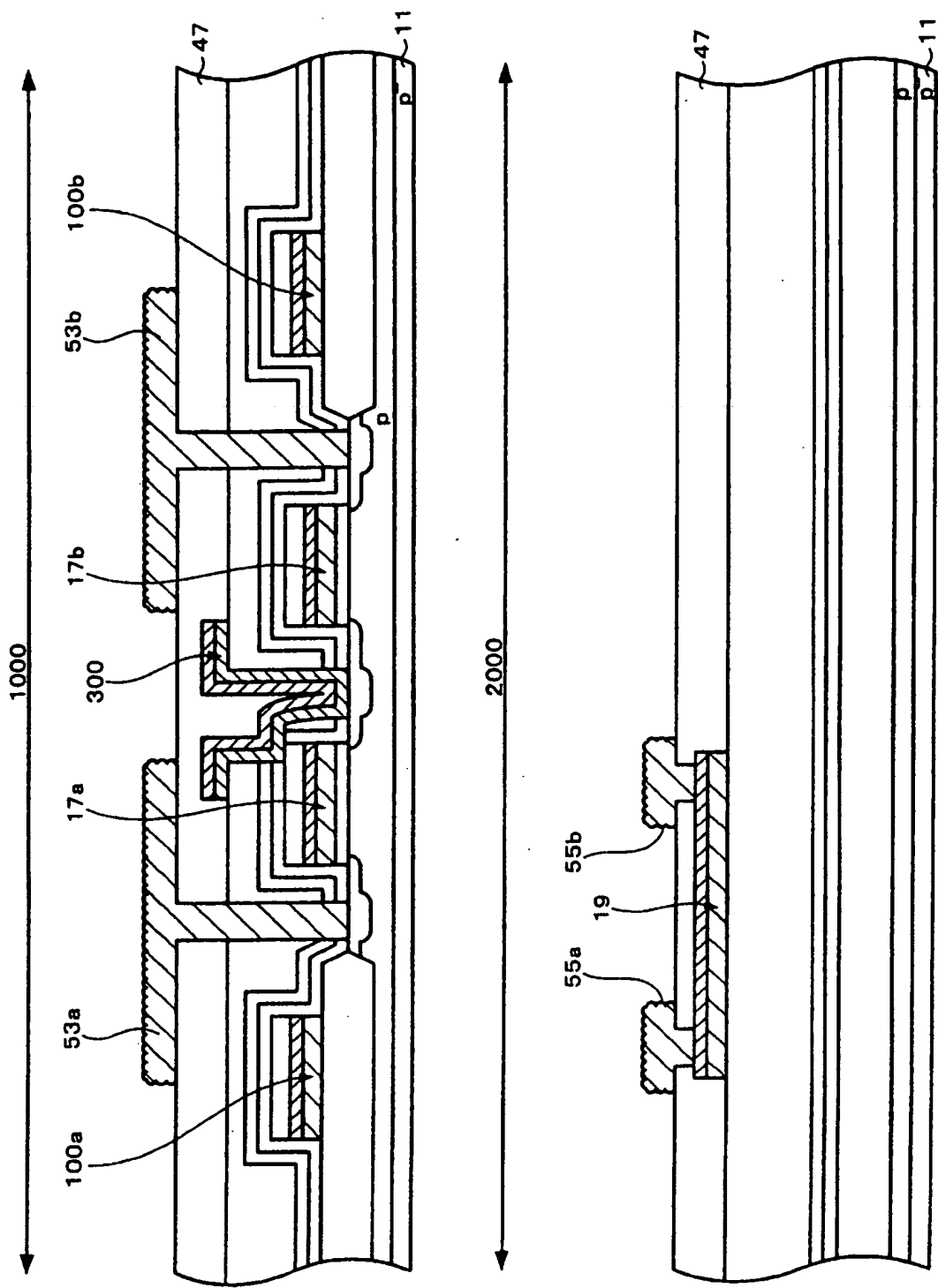


Fig. 9

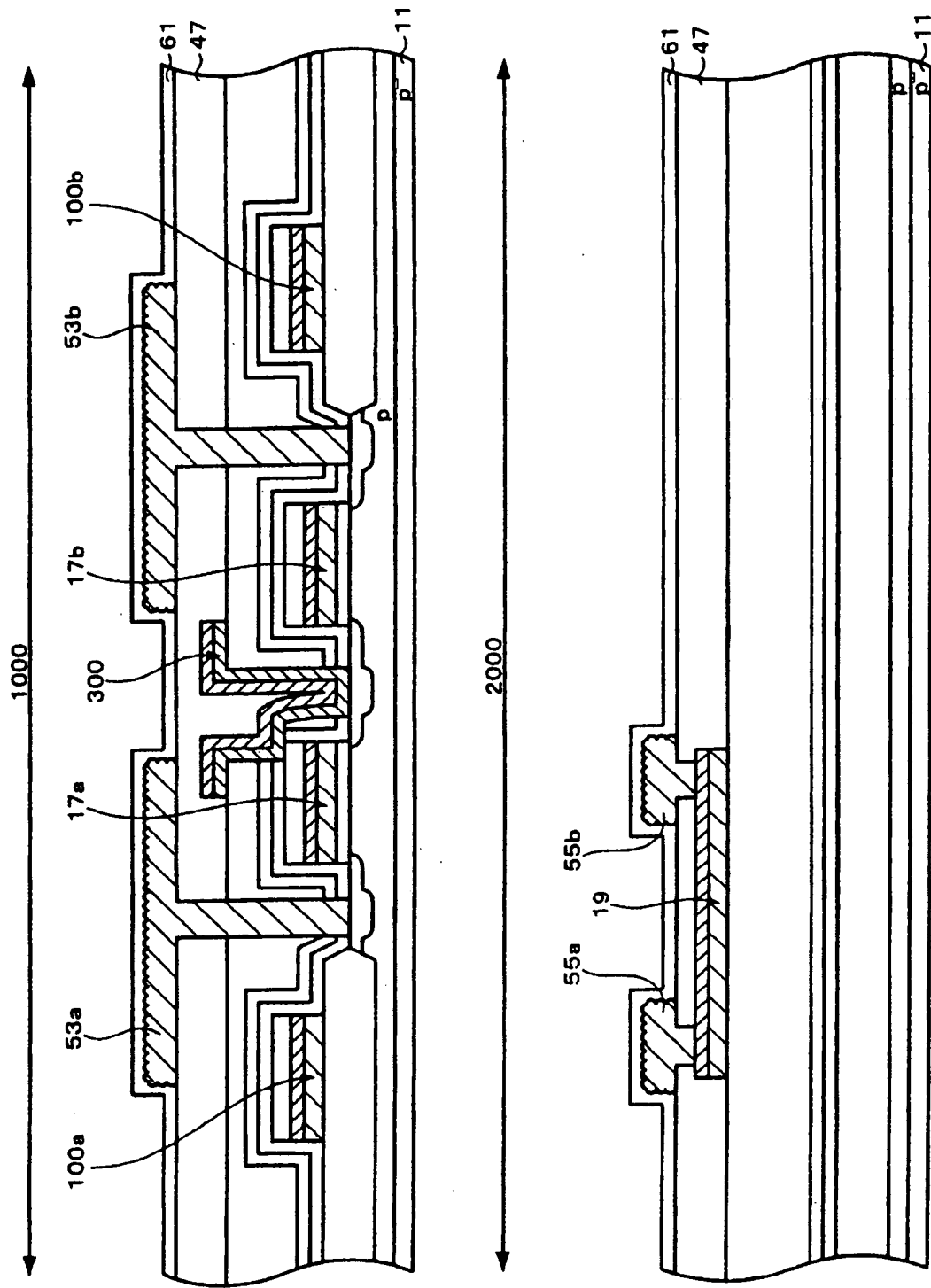


Fig. 10

FIG. 10 is a cross-sectional view of a portion of the device of FIG. 1, taken along line 10-10 of FIG. 1, showing the details of the first stage of the device. The device includes a substrate 11, a first stage 100a, a second stage 100b, and a third stage 100c. The first stage 100a includes a first electrode 17a, a first gate 17b, and a first channel 17c. The second stage 100b includes a second electrode 53a, a second gate 53b, and a second channel 53c. The third stage 100c includes a third electrode 61, a third gate 63, and a third channel 61. The device is shown in a cross-sectional view, with the substrate 11 at the bottom and the stages 100a, 100b, and 100c stacked on top of each other. The first stage 100a is shown in a cross-sectional view, with the first electrode 17a, the first gate 17b, and the first channel 17c. The second stage 100b is shown in a cross-sectional view, with the second electrode 53a, the second gate 53b, and the second channel 53c. The third stage 100c is shown in a cross-sectional view, with the third electrode 61, the third gate 63, and the third channel 61. The device is shown in a cross-sectional view, with the substrate 11 at the bottom and the stages 100a, 100b, and 100c stacked on top of each other.

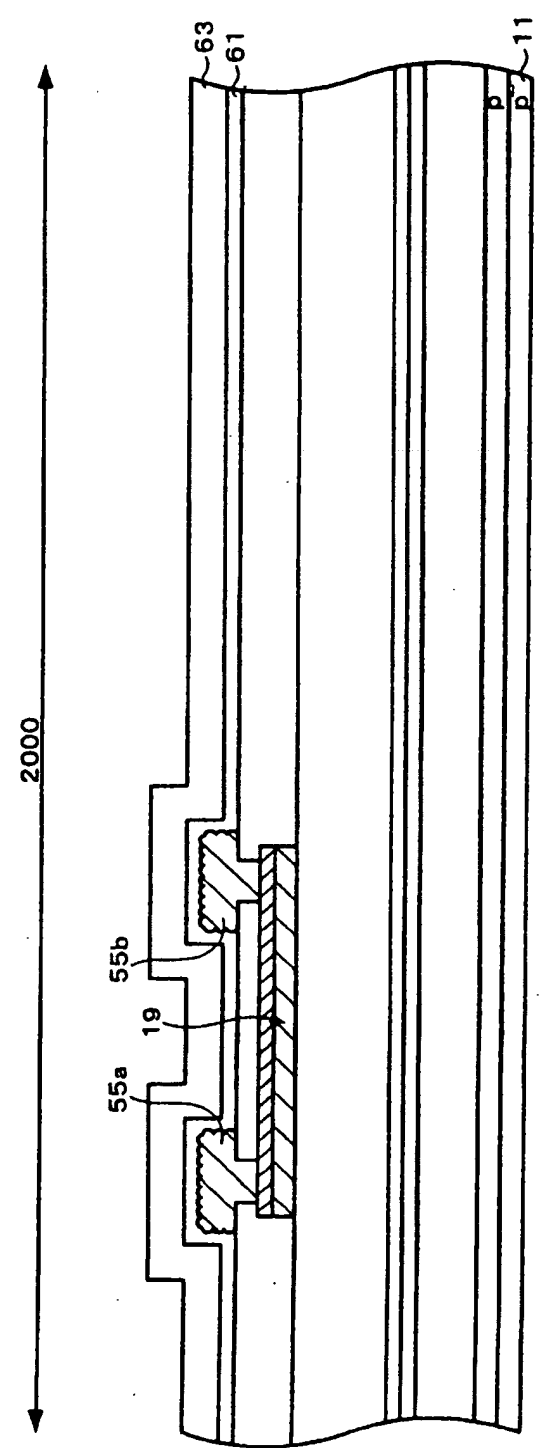
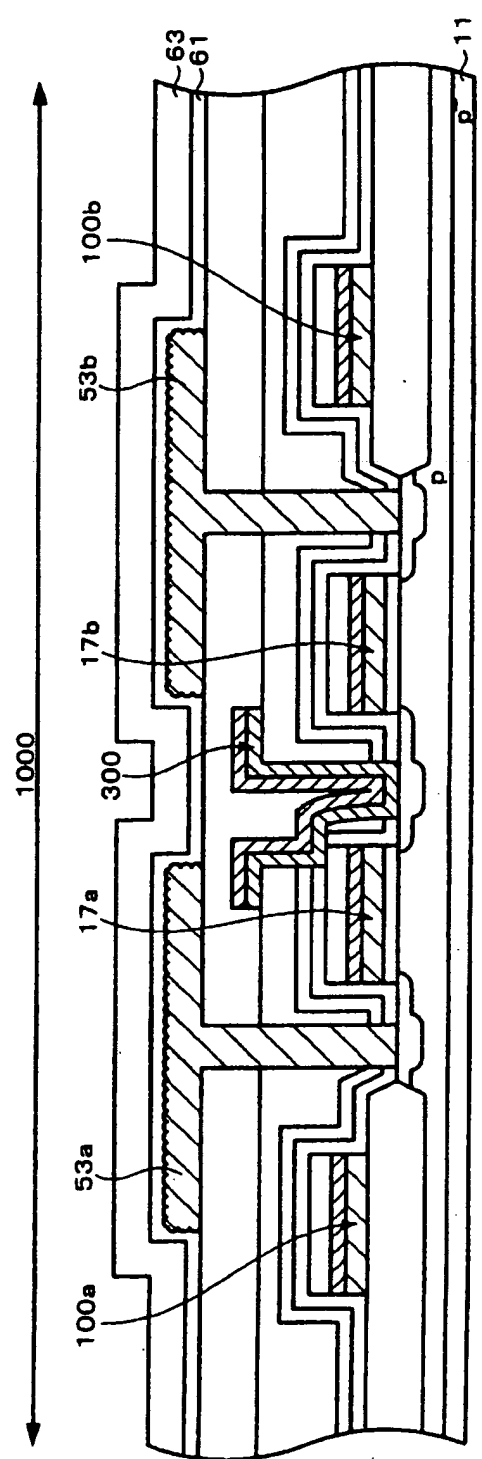


Fig. 11

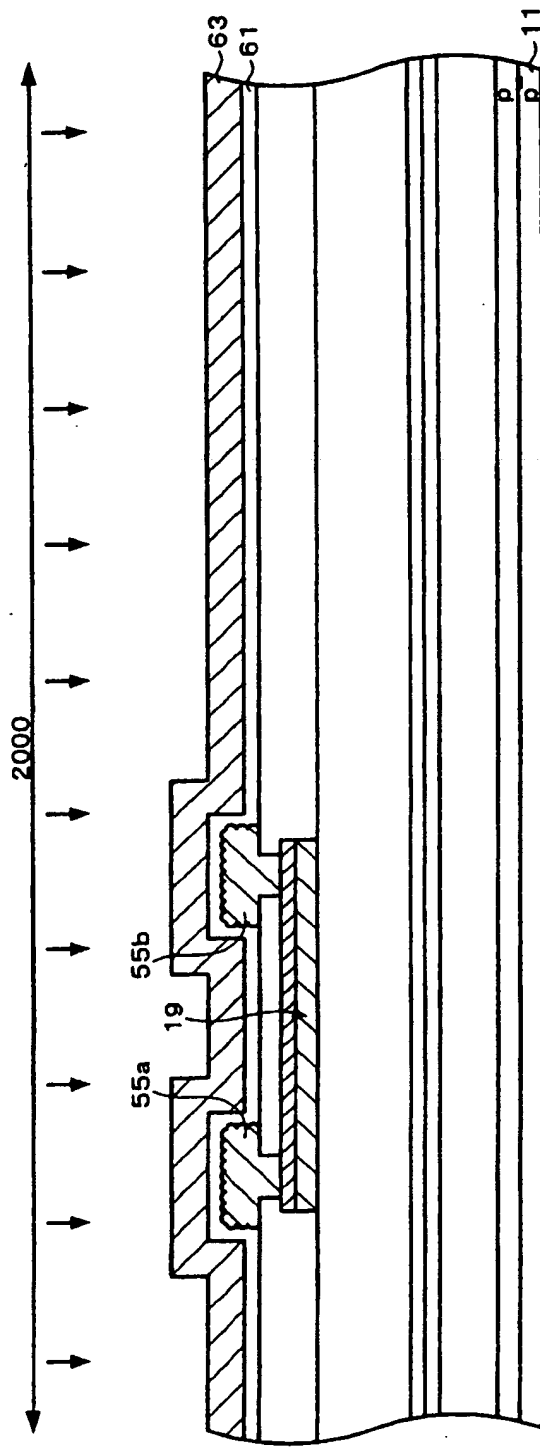
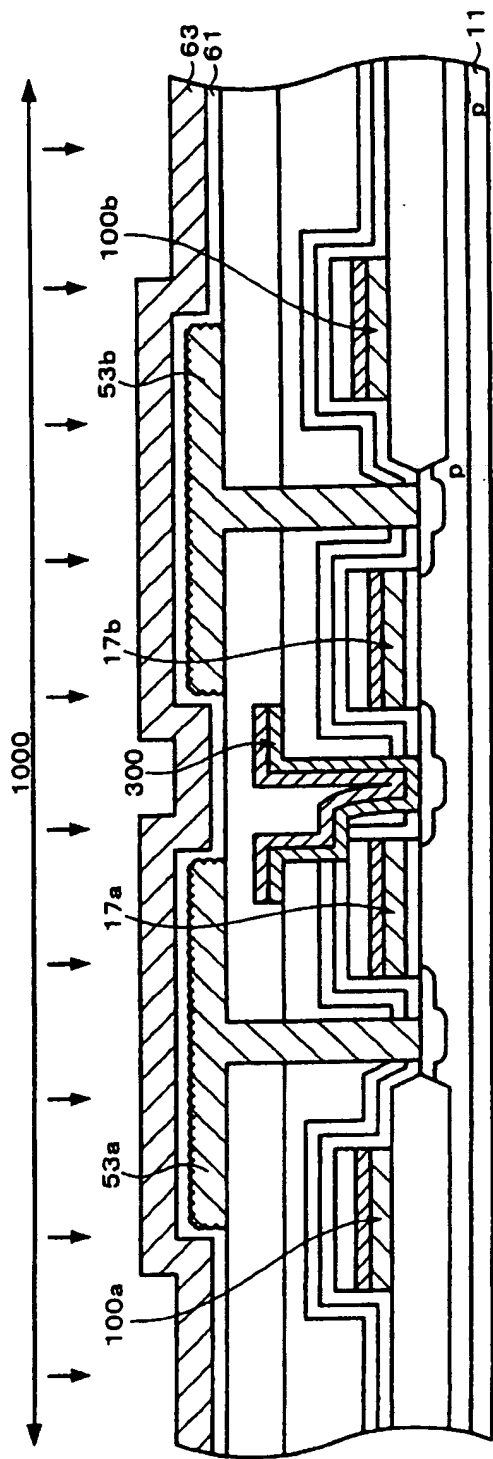


Fig. 12

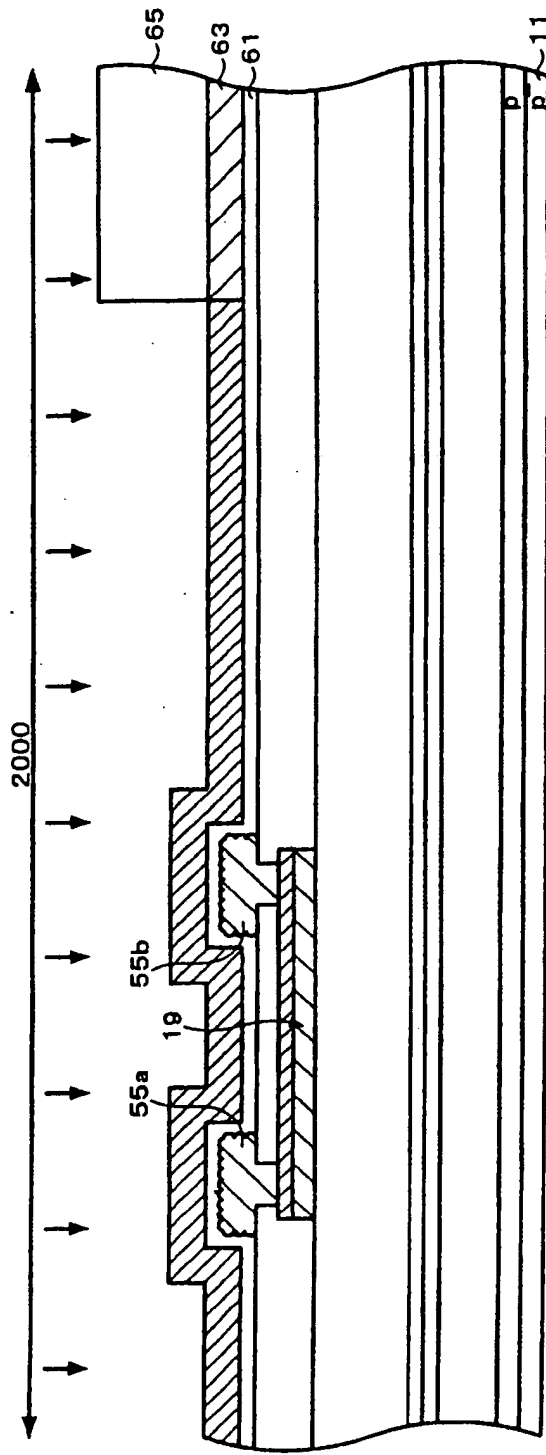
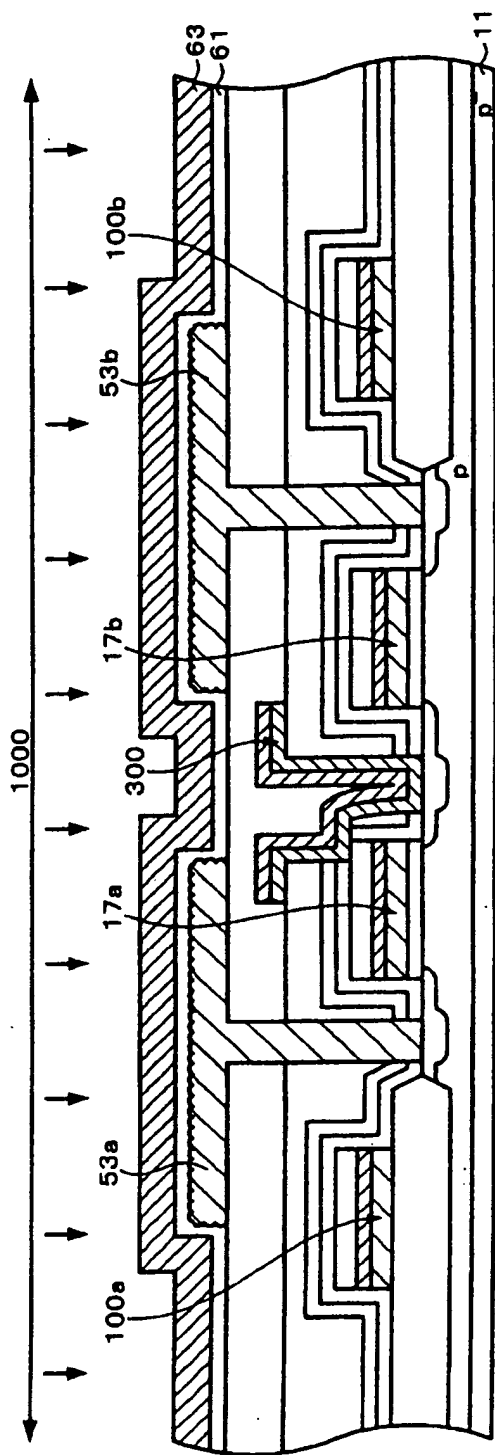


Fig. 13

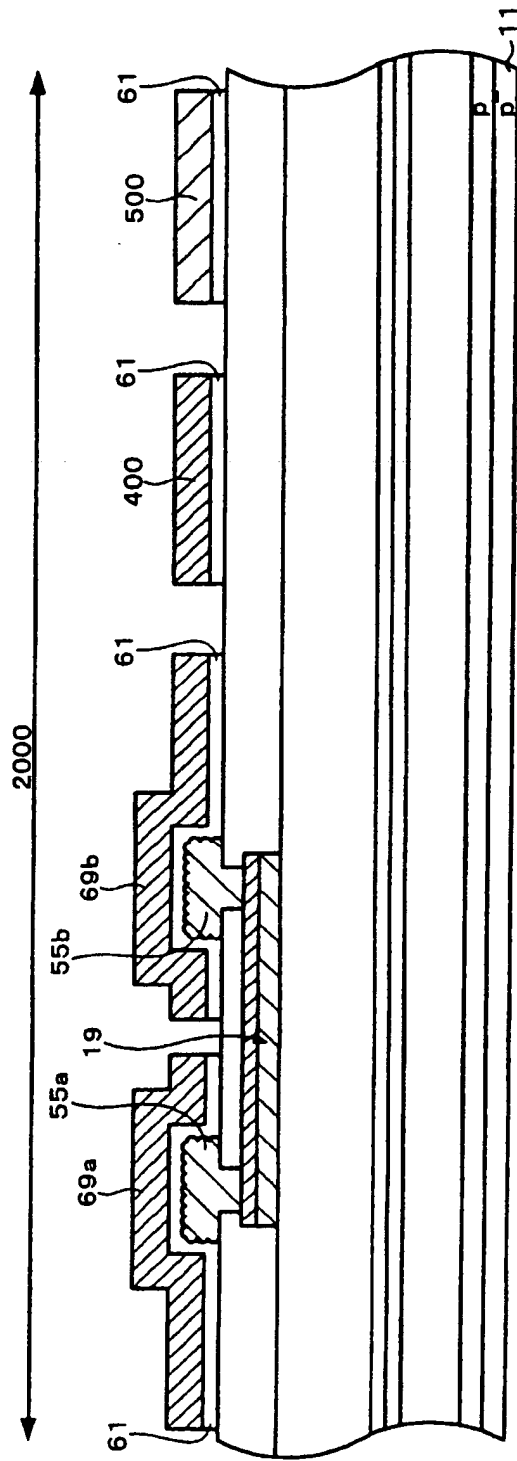
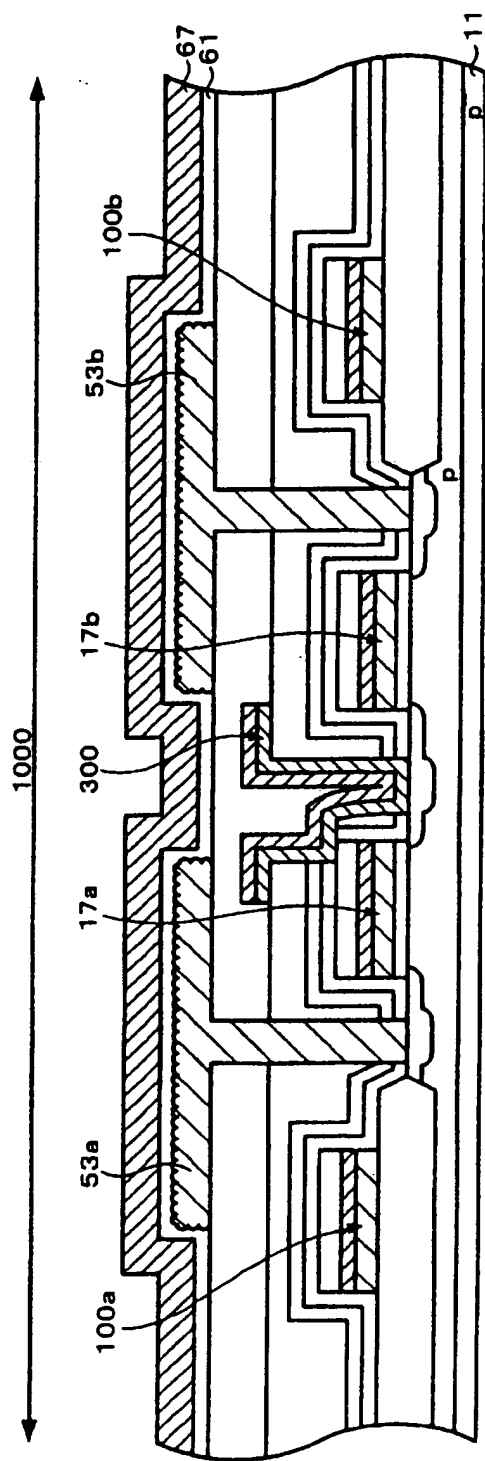


Fig. 14

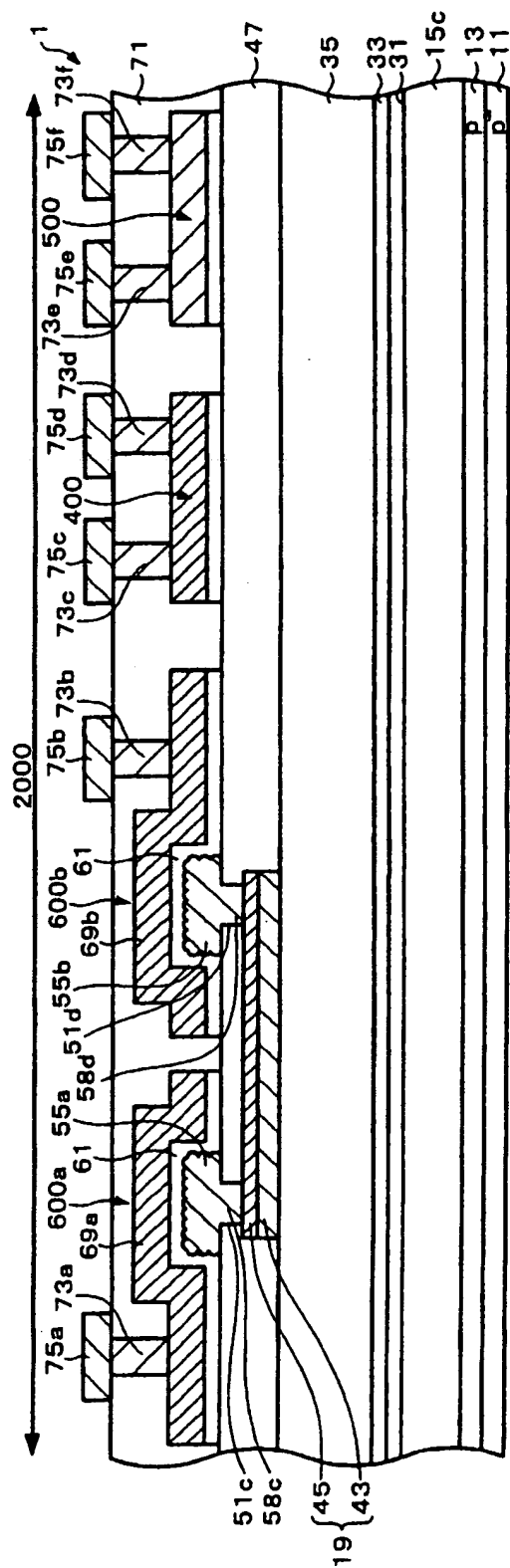
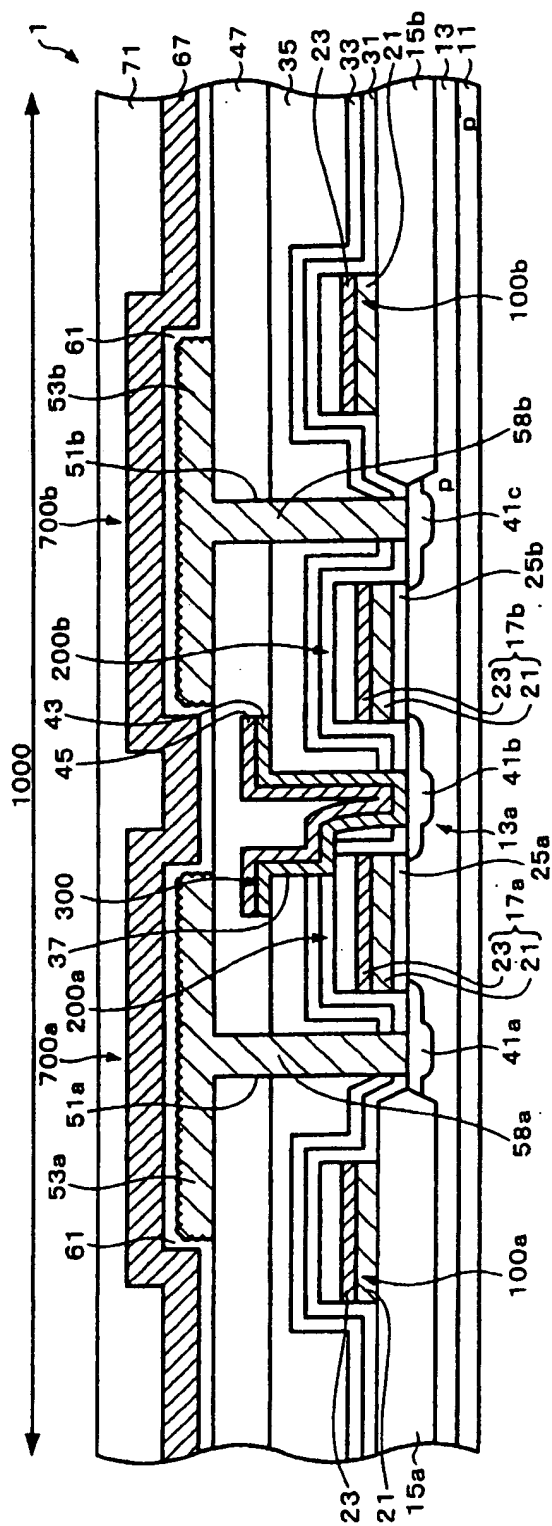


Fig. 15

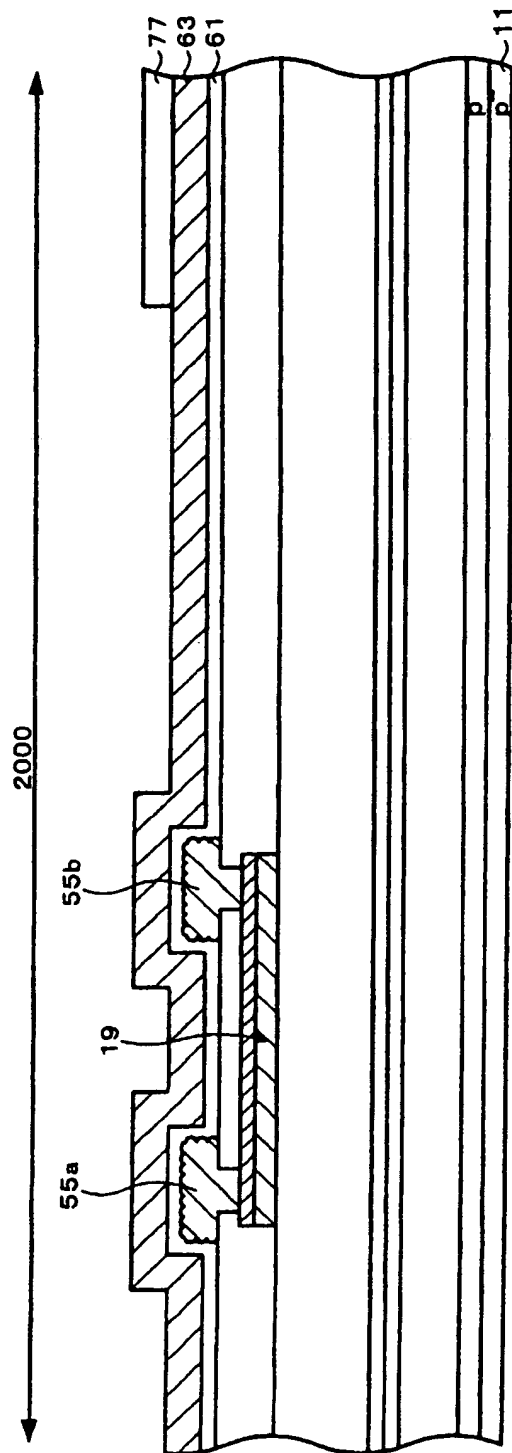
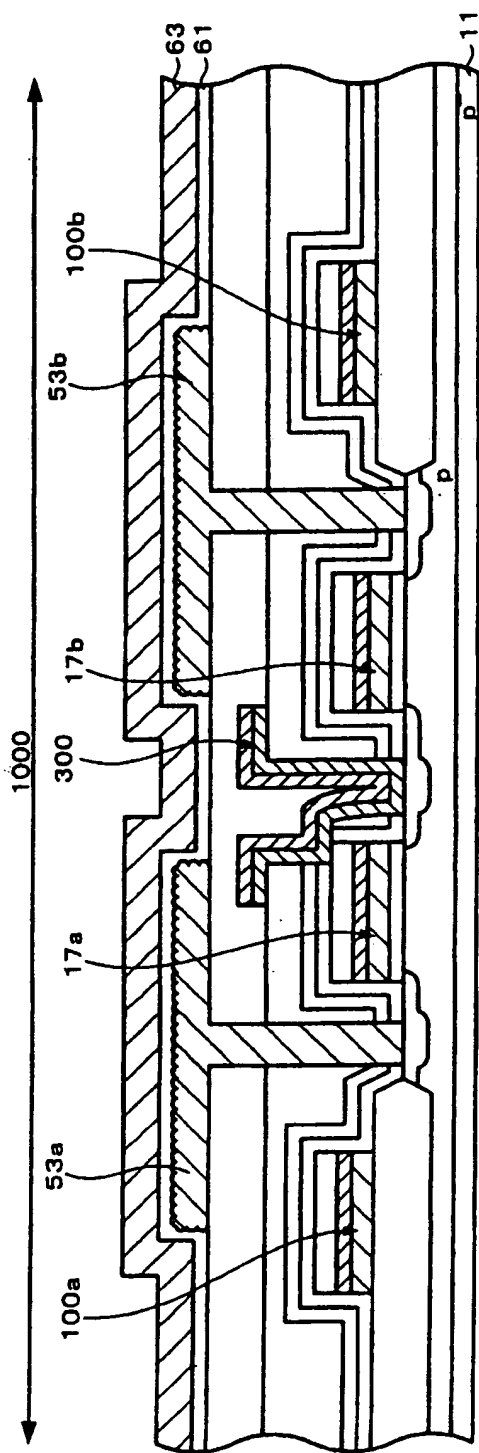


Fig. 16

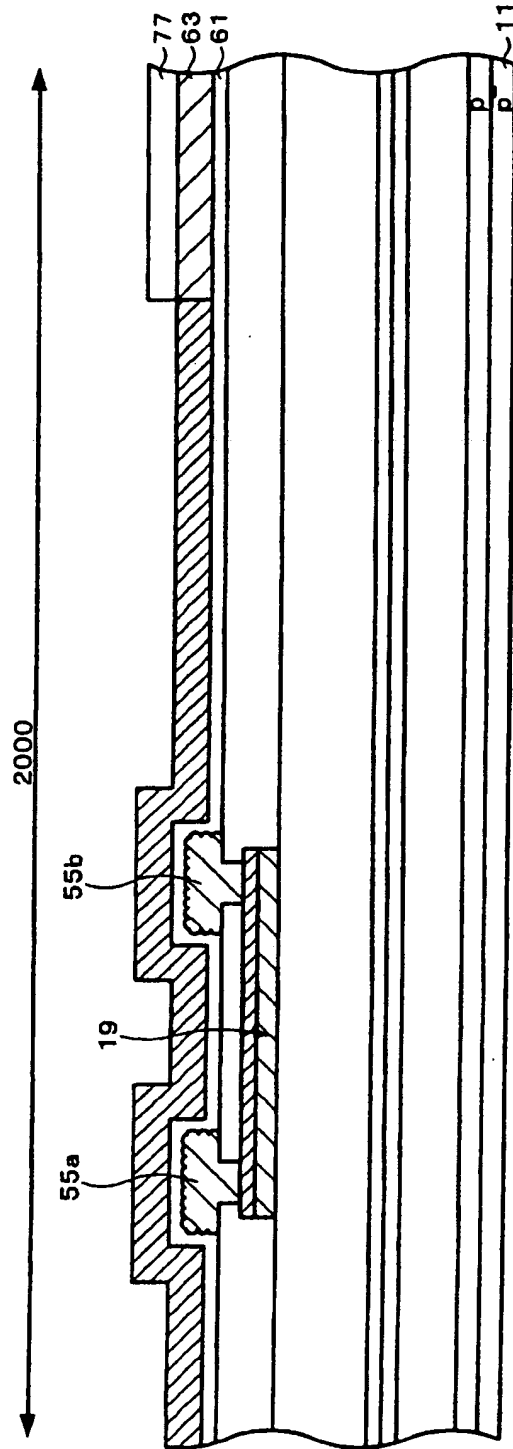
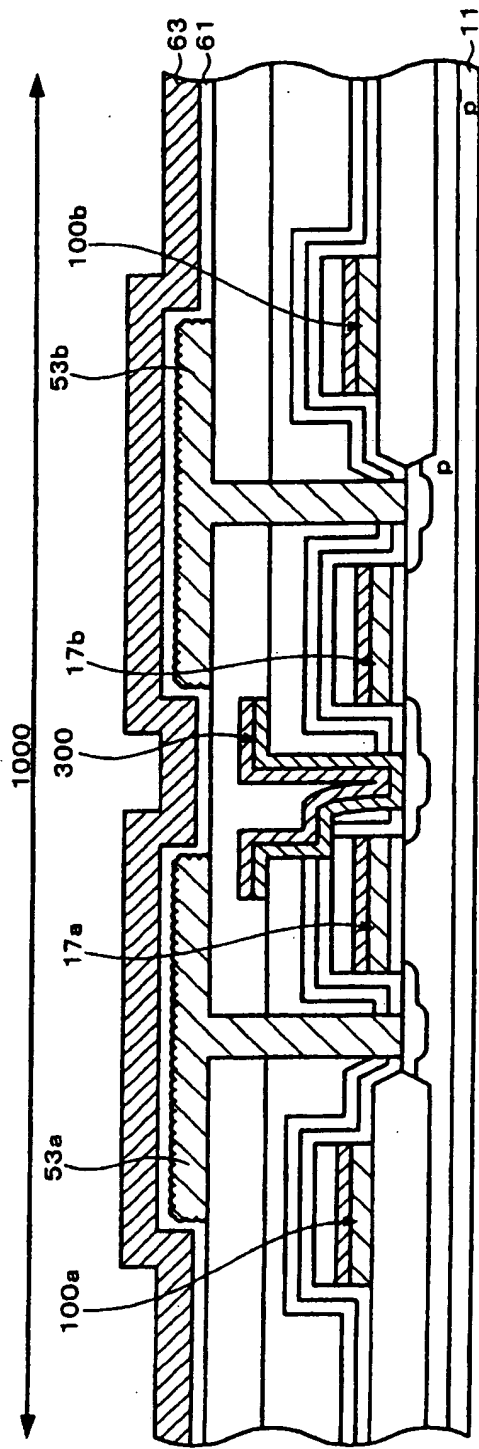


Fig. 17

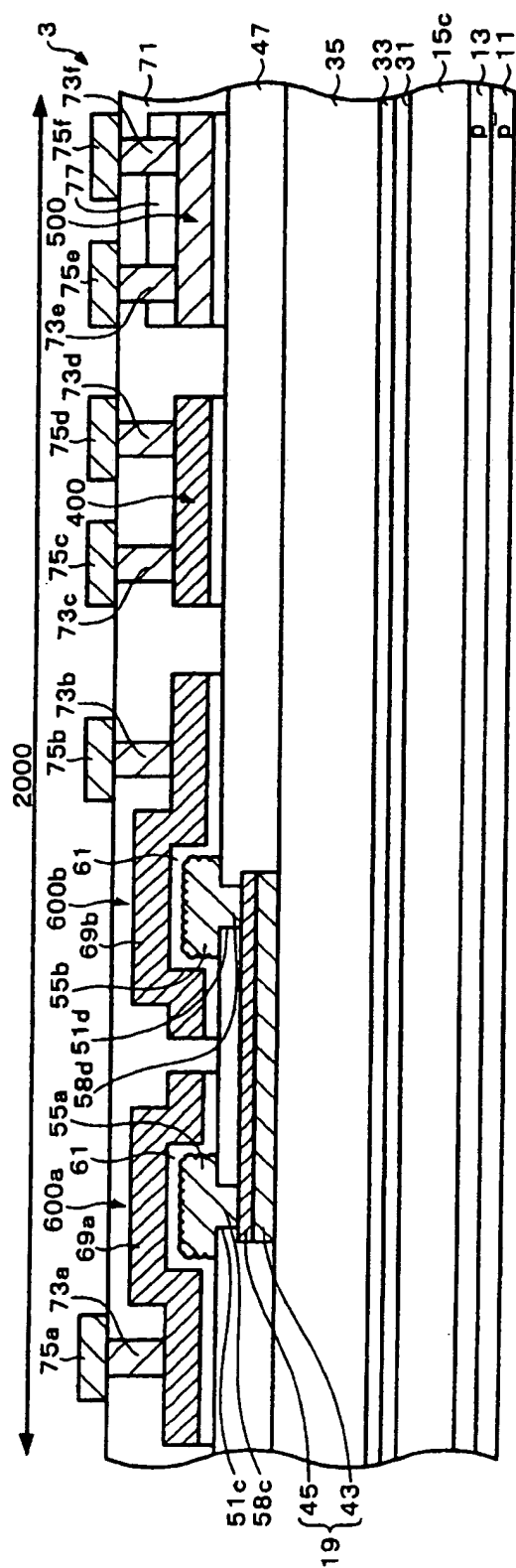
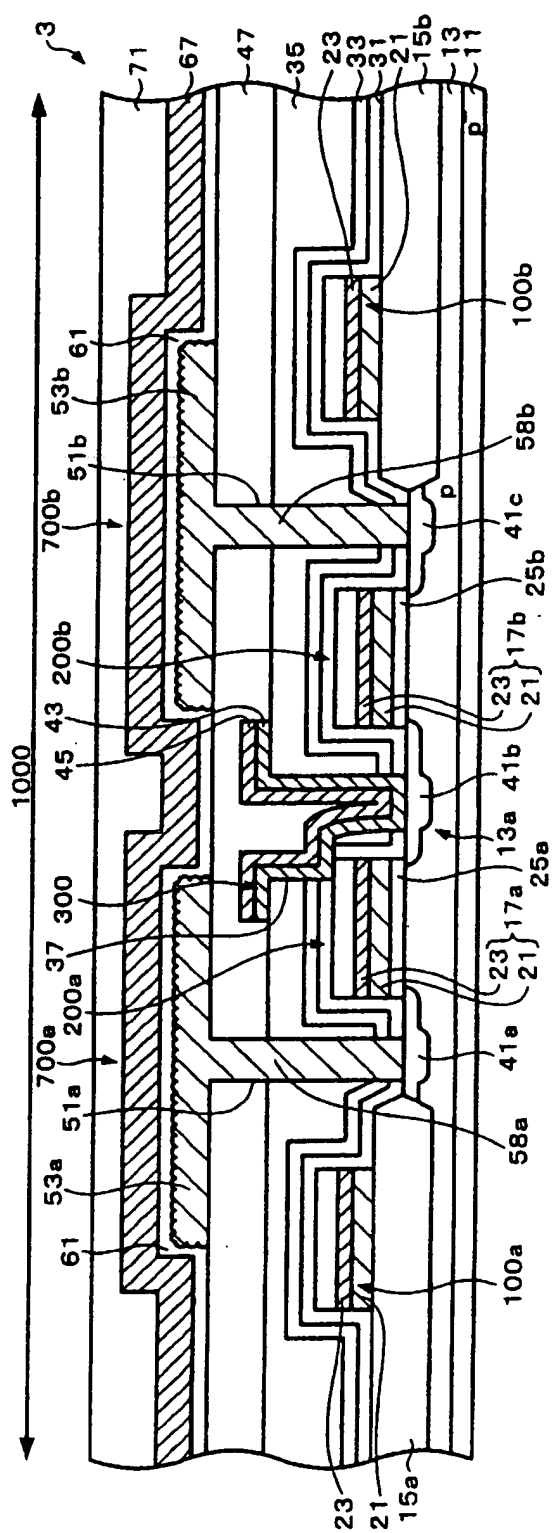


Fig. 18

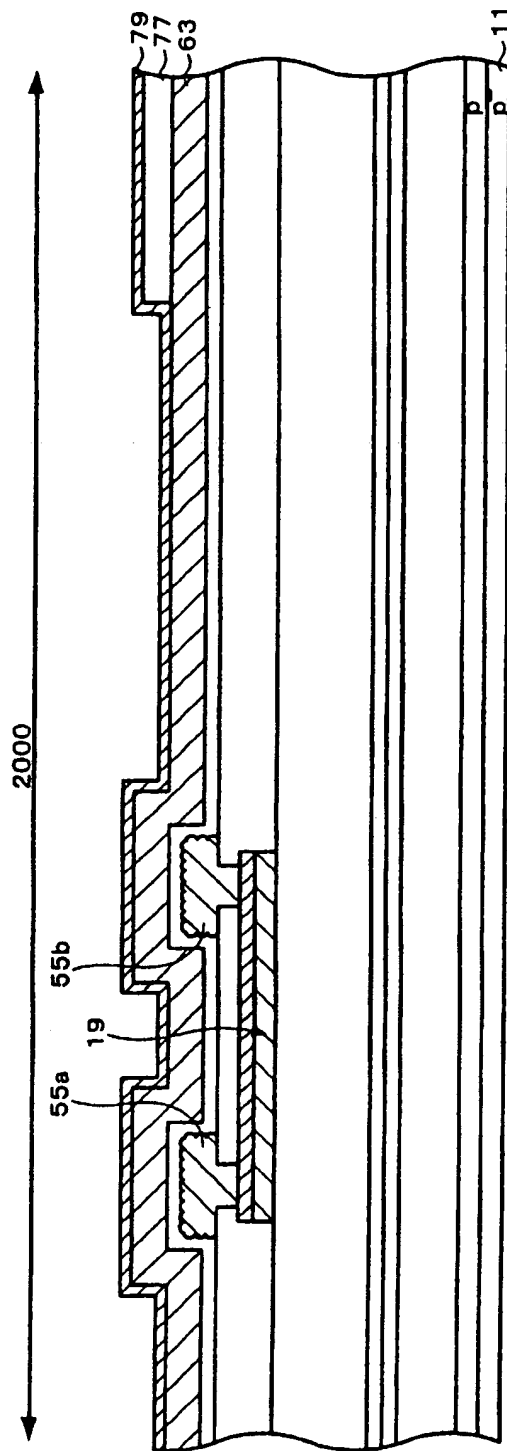
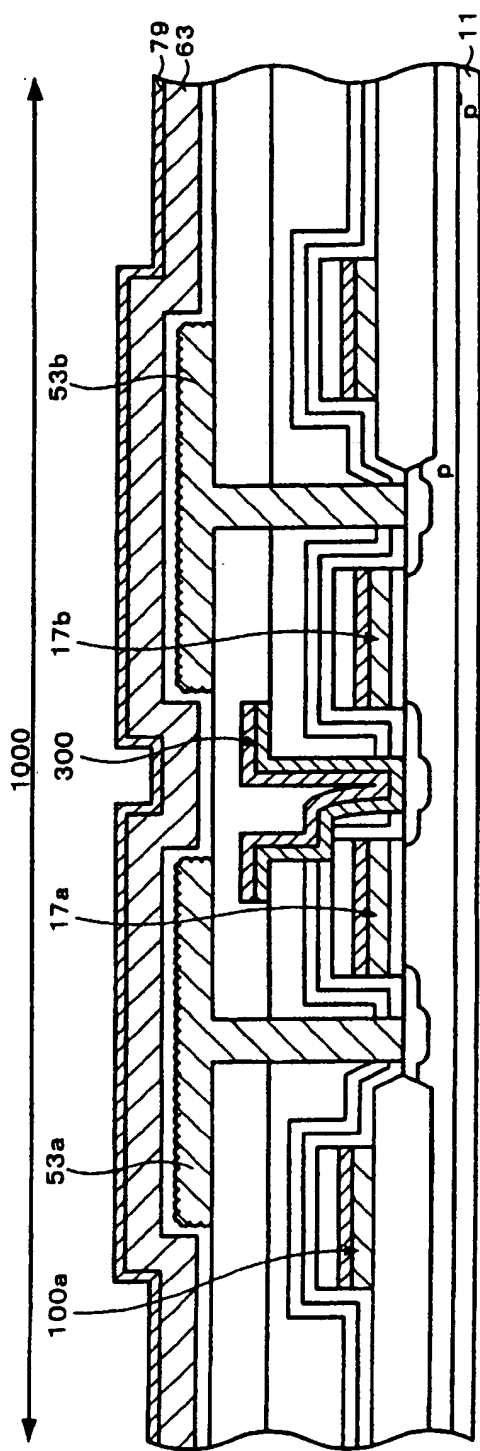


Fig. 19

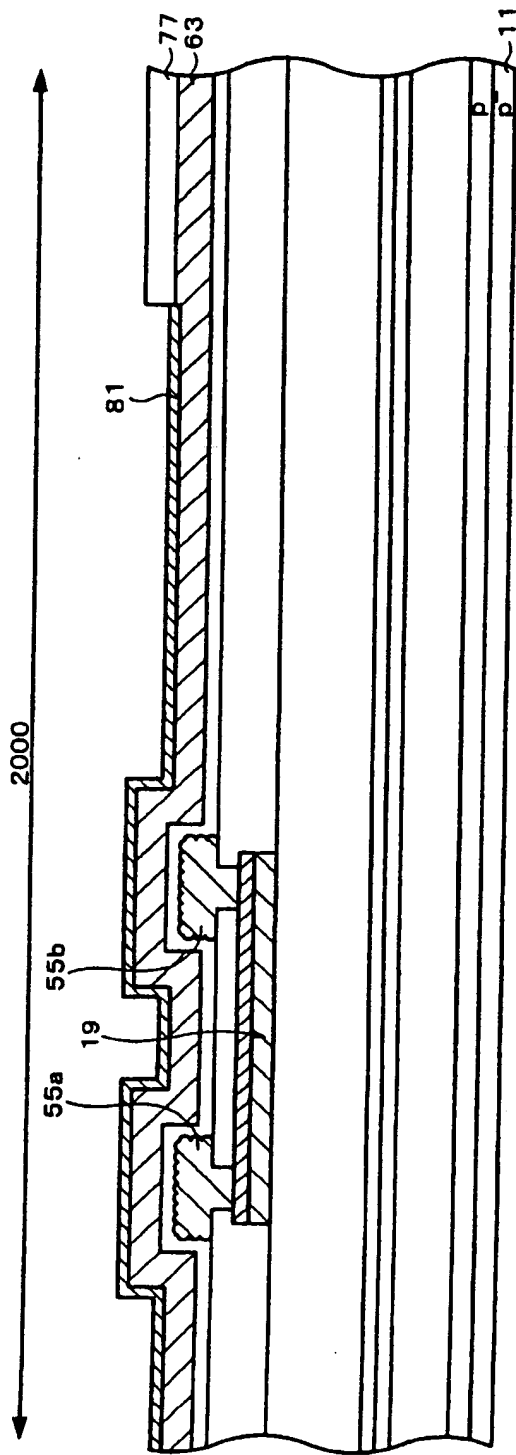
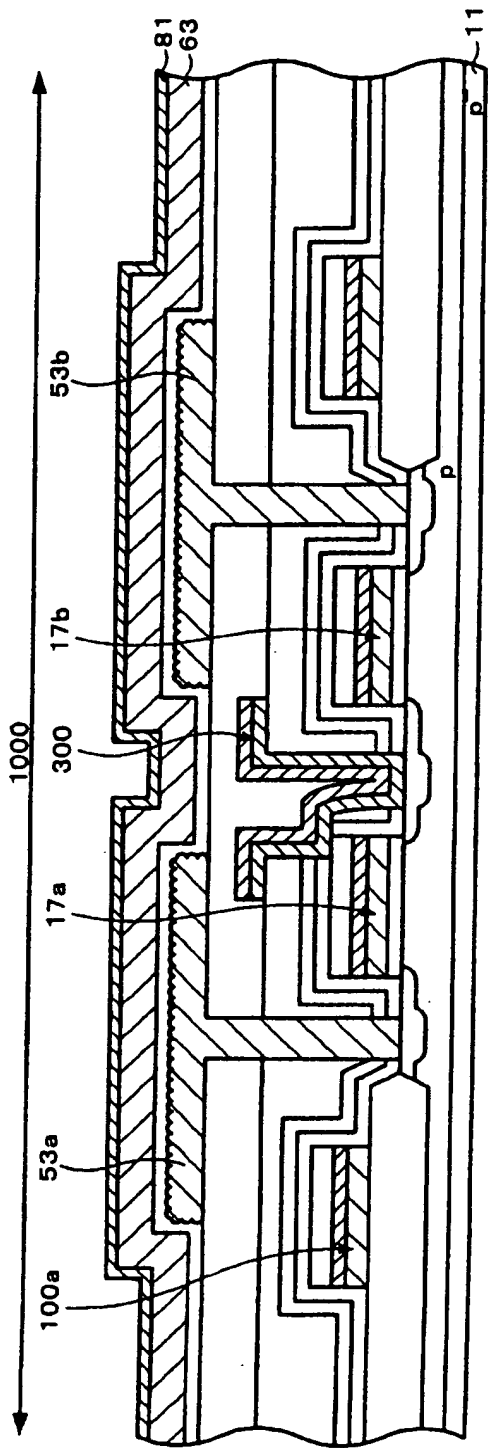


Fig. 20

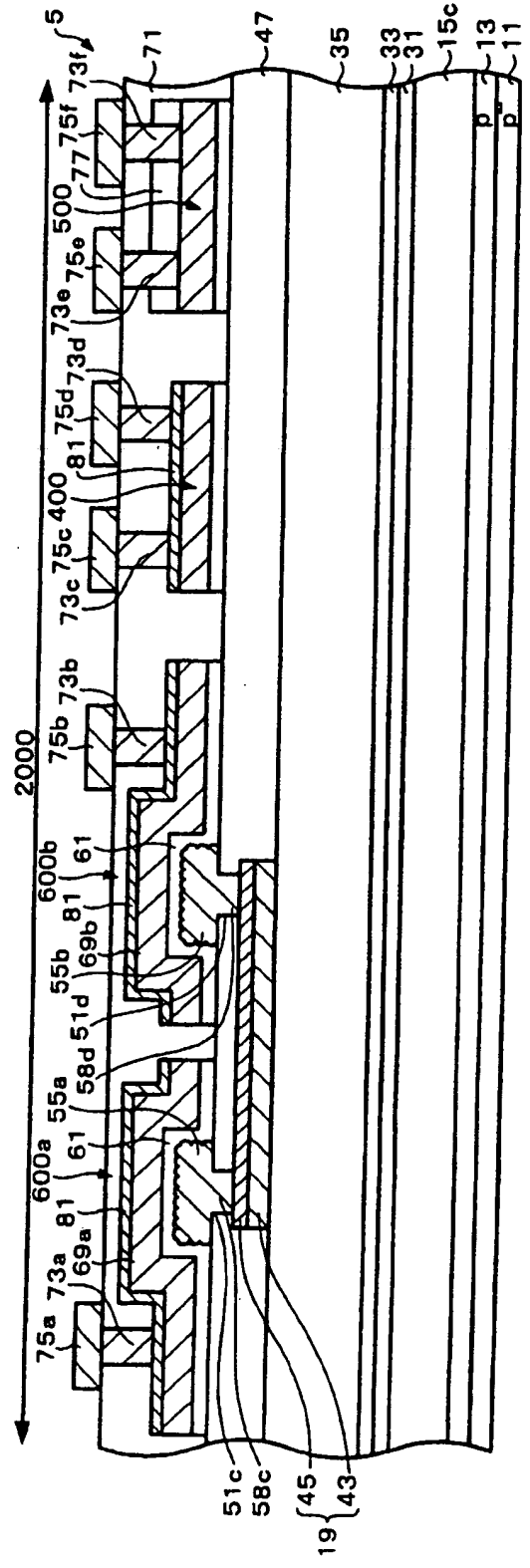
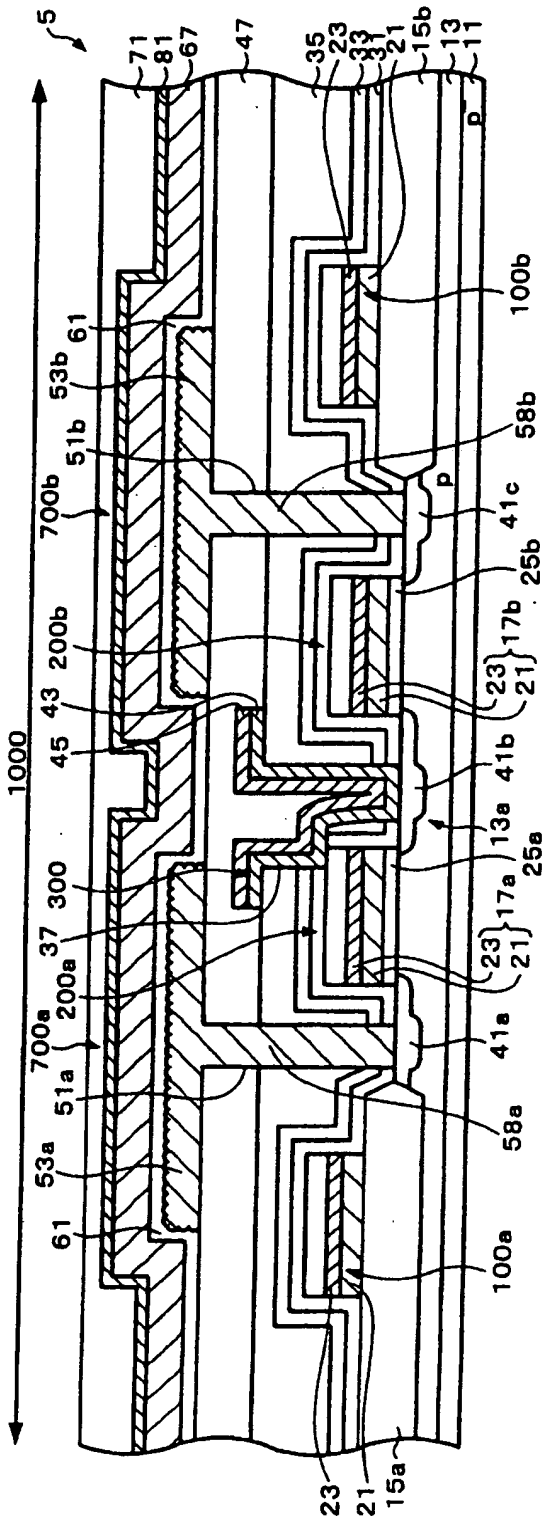


Fig. 21

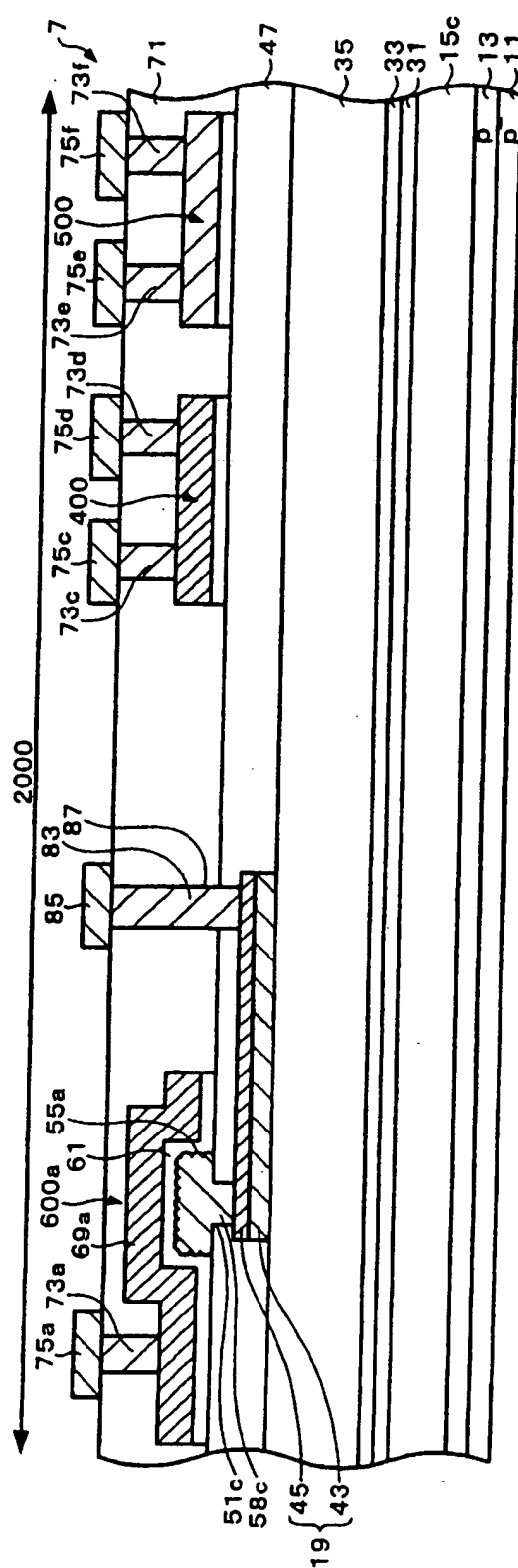
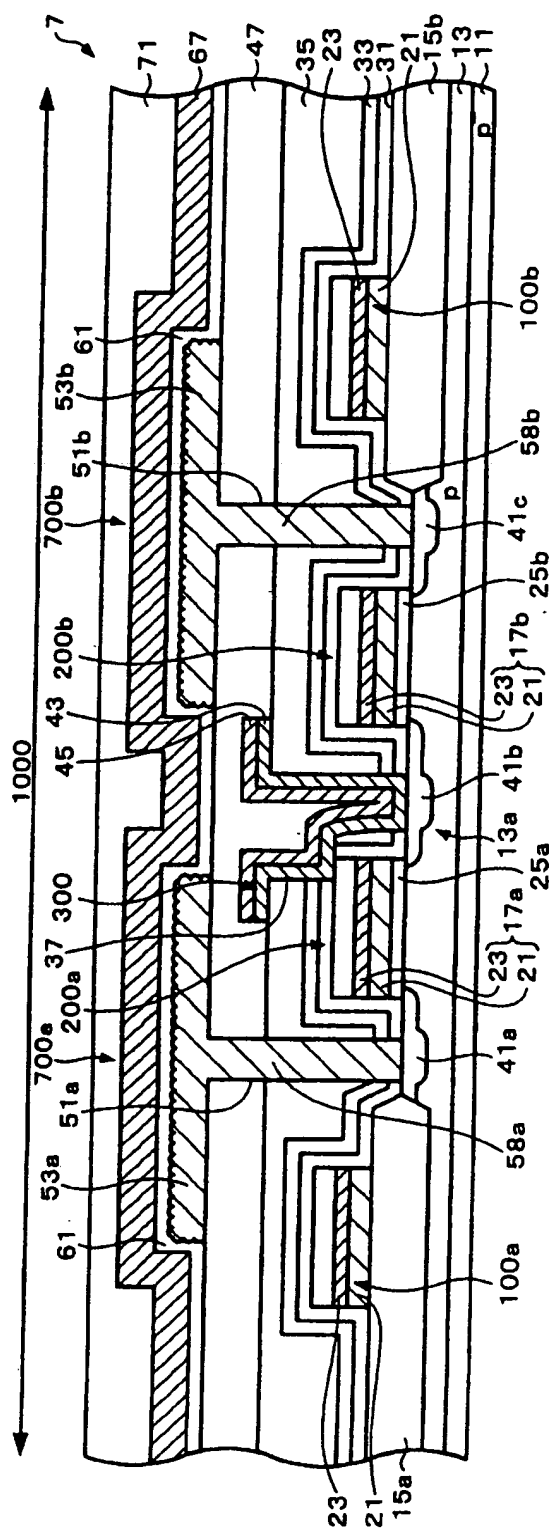


Fig. 22